



PATENT
P19002
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of) Examiner: Gopal C. Ray
Tsao, et al.)
Serial No.: 10/816,435) Art Unit: 2111
Filed: March 31, 2004)

For: INTERRUPT SCHEME FOR AN INPUT/OUTPUT DEVICE

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

TRANSMITTAL OF INFORMATION DISCLOSURE STATEMENT

Dear Sirs:

In accordance with 37 CFR §§ 1.56, 1.97, and 1.98, enclosed please find Form PTO-1449 listing the attached references which might be deemed material to the examination of the above-identified application.

A. Non-English Language References


- ☐ Enclosed is a search report for a counterpart application. The search report Examiner has provided comments on the relevancy of any non-English language references cited in the search report.
- ☐ The specification incorporates comments on the relevancy of Non-English language references.
- ☐ Set forth below are comments provided by the applicant's home country counsel on the relevancy of non-English language references:

- B. ☒ The information disclosure statement submitted herewith is being filed
- 1) within three months of the filing date of a national application other than a continued prosecution application under 1.53(d);

- 2) within three months of the date of entry into the national stage as set forth in 1.491 in an international application;
- 3) before the mailing date of a first Office Action on the merits; or
- 4) before the mailing date of a first Office Action after filing of a Request for Continued Examination under 1.114.

Because this submission is made prior to the mailing of a first Office Action on the merits, it is believed that no fee is due. Please contact the undersigned with any questions in this regard.

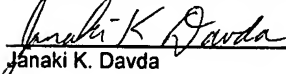
Respectfully submitted,


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2/1/06
Date

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I hereby certify that this correspondence is being deposited with the United States Postal Service on the date indicated above and is addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.


Janaki K. Davda
(Date) 2/1/06

FORM PTO-1449 INFORMATION DISCLOSURE CITATION IN AN APPLICATION (Use several sheets if necessary) 77.96	Docket Number (Optional) P19002	Application Number 10/816,435
	Applicant Tsao, et al.	
	Filing Date March 31, 2004	Group Art Unit 2111

U.S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE

FOREIGN PATENT DOCUMENTS

	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
						YES	NO

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

	US2005-228920 published October 13, 2005 (atty dkt P19014)
	US Patent Application No. 11/106,824 filed 4/15/2005 (atty dkt P19611)
	IEEE Standard for Information Technology 802.3, Part 3: Carrier Sense Multiple Access with Collision Detection (CSMA/CD) Access Method and Physical Layer Specifications; Institute of Electrical and Electronics Engineers, Inc., March 8, 2002; 33 pp.
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	PCI Local Bus Specification Rev. 2.3: PCI Engineering Change Notice - MSI-X, June 10, 2003; Summary pp 1-2; Chapter 6, "Configuration Space," pp. 189 - 209.
	PCI Specifications, PCI-SIG 2004; web pages including March 20, 2002 news release "PCI-SIG Releases New PCI Version 2.3 Local Bus Specification for Migration to Low-Voltage Designs," 18 pp. Available from the Internet at <URL: http://www.pcisig.com >.
	Weber, et al., "Fibre Channel (FC) Frame Encapsulation," Network Working Group, RFC:3643, December 2003; 17 pp.
	Culley, et al. "Marker PDU Aligned Framing for TCP Specification (Version 1.0)," Release Specification of the RDMA Consortium; October 25, 2002; 32 pp.
	Hilland, et al. "RDMA Protocol Verbs Specification (Version 1.0)," Release Specification of the RDMA Consortium; April 2003; 243 pp.
	Recio, et al. "An RDMA Protocol Specification (Version 1.0)," Release Specification of the RDMA Consortium; October 2002; 60 pp.
	Shah, et al. "Direct Data Placement over Reliable Transports (Version 1.0)," Release Specification of the RDMA Consortium; October 2002; 35 pp.
	Anderson, Don, et al., eds. "PCI System Architecture," 4 th ed.; TOC pp. v - xlii; Intro. pp 1-6; Chapter 1 pp. 7-13; Chapter 2 pp 15-21; 1999.
	Window Platform Design Notes, "Interrupt Architecture Enhancements in Microsoft Windows ...," ©2003 Microsoft Corporation; 42 pp.

EXAMINER	DATE CONSIDERED
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EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP § 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the applicant.